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TOP 20 WEB RESULTS out of about 1,730 (What's this?)

1. <u>Cookies and **HTML** Tags Goals</u> (PDF)
   ... 6 11/17/1999 © 1999, Badri R. Narayanan 11 **HTML Tag** - **Test Plan** • Test Cases –
   Create **HTML tag** – Encode **HTML tag** – Decode **HTML tag** – Alter **HTML tag** ...
   www.cs.stevens-tech.edu/~badri/CS640/HTMLCookies.pdf - <u>View as html</u>

2. <u>Doxygen MailingList: Request for test criteria documentation su</u>
   ... cfg diagrams/**html**/index.**html**: diagrams_a.h ... test +GENERATE_TAGFILE = test.**tag**
   +GENERATE_LATEX
   = NO ... bool hideTestPlanFlag; // hide **test plan** documentation static ...
   www.xeqt.com/doxygen/list-000714-000826/0052.html - 18k - <u>Cached</u>

3. <u>Plugins **Test Plan**</u>
   ... goals as mentioned in section 1 of the **test plan**. ... com; Load
   www.powershot.com/splash.**html**.
   For Realplayer: ... via EMBED **tag**; via OBJECT **tag**; standard content; standard ...
   www.mozilla.org/quality/browser/front-end/testplans/pluginstestplan.html - 36k - <u>Cached</u> -
   <u>More pages from this site</u>

4. <u><!--title-->Wal-Mart Shelves RFID **Test Plan**<!--/title--></u>
   Wal-Mart Shelves RFID **Test Plan** ... By John Cox July 23, 2003. So-called smart shelves
   can read the RFID **tag** when an individual item is placed on the shelf and when ...
   www.crmdaily.com/perl/story/21957.html - 10k - <u>Cached</u>

5. <u>[Webtest] WebTest-http-request-recorder? Verifying whole **HTML**- ...</u>
   ... (a new validation **tag**, the "diff-**tag**" has to ... He validates (optically) each > response
   (**HTML**-Page) according to the description in the **test** > **plan**. ...
   lists.canoo.com/pipermail/webtest/2002q4/000345.html - 7k - <u>Cached</u> - <u>More pages from
   this site</u>

6. <u>Latest STIS **Test Plan** on Paper</u>
   Latest STIS **Test Plan** on Paper (already changed, natch). ... N2-6 IM Pt Lamp Testing
   At a safe slit setting (compare with band 1 time-**tag** testing), take time-**tag** ...
   hires.gsfc.nasa.gov/stis/stiscal/plans/plan_0413.html - 29k - <u>Cached</u>

7. <u>Moxy - **Test Plan**</u>
   ... This document is an overall **test plan** that will serve as the ... Comment **Tag** Removal
   filter, Removes comment tags (<!-- cmnt -->) tags from an **HTML** page. ...
   www.cs.hut.fi/~ctl/moxy/documents/ts.html - 32k - <u>Cached</u>

8. <u>Macromedia - DevNet: Unit Testing ColdFusion Components with ...</u>
   ... Your test case will automatically generate an **HTML** table of test results. ... can also
   be placed in a directory under a ColdFusion mapping, or in a custom **tag** root ...
   www.macromedia.com/devnet/mx/coldfusion/articles/cfunit.html - 31k - <u>Cached</u> - <u>More
   pages from this site</u>

9. <u>CVS test plan (CE)</u>
   ... installing, look at this pages: http://www.loria.fr/~molli/cvs-index.**html** http://download ...

· one
or more branches for the given file (Look at Add **Tag** command to ...
qa.netbeans.org/man-suites/guide_cvs.html - 63k - <u>Cached</u>

10. <u>CUPS Configuration Management Plan</u> 🖻
... Software testing shall be conducted according to the CUPS Software **Test Plan**, CUPS-
STP ... cmp.**html**,v
1.9 2003/05/27 17:40:43 mike Exp $" **tag**: ... Id: cmp.**html**,v 1.9 ...
www.cups.org/cmp.html - 31k - <u>Cached</u>

11. <u>Testing Without a Formal **Test Plan**</u> 🖻
... During your pedal-to-the-floor, no-**test-plan** testing project, boundary ... Everyone's
favorite: enter in **HTML** code. ... name in BLINK tags, enter in an IMG **tag** for a ...
www.philosophe.com/testing/without_testplans.html - 18k - <u>Cached</u>

12. <u>xml-dev - ID and **tag** value uniqueness, how?</u> 🖻
... 1998-10-15</duedate> <whom>Joe Bob</whom> </assigned> <summary
status="closed"> <resolution>**Test**
plan modified and ... Follow-Ups: ID and **tag** value uniqueness, how ...
lists.xml.org/archives/xml-dev/199810/msg00513.html - 10k - <u>Cached</u>

13. <u>Standard Operating Procedures, Volume 2</u> 🖻
... denoted as follows: **HTML Tag**, Denotation, Required Inspection. <Hn>,
For ... of anchor target. Inspection **Test Plan** and Record. A functional ...
www.csc.calpoly.edu/~gfisher/classes/205/handouts/sop-testing.html - 11k - <u>Cached</u> - <u>More</u>
<u>pages from this site</u>

14. <u>Sample Year 2000 **Test Plan**</u> 🖻
... **html**; GA Tech Application **Test Plan** Guidelines;
http://danshiki.oit.gatech.edu/**html**/documents/**tag**/Appl_Test_Plan_Sect3.**html**;
SAIC ...
www.state.ma.us/y2k/Archive/ProjPlanning/testplan.htm - 33k - <u>Cached</u> - <u>More pages from</u>
<u>this site</u>

15. <u>Wal-Mart shelves RFID **test plan**</u> 🖻
... Wal-Mart shelves RFID **test plan**. ... These so-called smart shelves can read the RFID
tag when an individual item is placed on the shelf and when it's removed. ...
www.nwfusion.com/news/2003/0714walmart.html - 53k - <u>Cached</u>

16. <u>Top 20 Web site analysis: Introduction</u> 🖻
... Number of **HTML** elements includes all tags Number of bytes size of page; excludes
images, style sheets, etc. Number of images specified by <IMG> **tag** Number of ...
www.veritest.com/bi/cont2000/200003/top20int.asp?visitor=X - 69k - <u>Cached</u> - <u>More pages</u>
<u>from this site</u>

17. <u>WebCGM Conformance **Test Plan**</u> 🖻
... OBJECT **tag** in **HTML** tests (as opposed to non-standard EMBED)? [Preliminary: yes,
but construct them minimally unless they are the point of the test.]; ...
www.cgmopen.org/technical/testing/task2-report-TOC.html - 95k - <u>Cached</u>

18. <u>OJI **Test Plan**</u> 🖻
... Client QA Component **Test Plan**. ... Applet within <APPLET> **tag** should load fine.
Functional
and ... Tags; Lifecycle of an applet; Liveconnect functionality. **HTML** tags would ...
website-beta.mozilla.org/quality/browser/front-end/testplans/ojitestplan.html - 16k - <u>Cached</u>
- <u>More pages from this site</u>

19. __HTML 4 For Dummies - Bonus Chapter 4, Section 4__ 图
... pick up your Web page if you use the <!DOCTYPE> **tag** with a reference to **HTML** 4.0
until **HTML** 4.0 is ... These sketches are the first documents in your **test plan**. ...
www.lanw.com/html4dum/h4d4e/extras/ex04/e04s04.htm - 25k - Cached

20. __TEST PLAN FOR PSD EM__ 图
... critical parameters of the PSD system since noise triggers can easily exceed the
expected event rates, leading directly to an exceed in time-**tag** rates sent to ...
www.cesr.fr/~jurgen/psd/doc/um_psd_em_1.1/um_psd_em_1.1.html - 101k - Cached

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1 2 3 4 5 6 7 8 9 10 ▶ **Next**
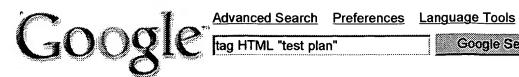
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www.mozilla.org/quality/browser/front-end/ testplans/pluginstestplan.html - 36k - Nov
22, 2003 - Cached - Similar pages

Intelligent Send Feature **Test Plan**
... Compose rich text using **HTML**; Option **tag** leaves as Auto Detect (default);
Select default (Ask me what to do) from Preference formatting; ...
www.mozilla.org/quality/mailnews/tests/ machv-mn-intelligentsend.html - 31k - Nov 22, 2003 - Cached -
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Wal-Mart Shelves RFID **Test Plan** ... By John Cox July 23, 2003. So-called smart shelves
can read the RFID **tag** when an individual item is placed on the shelf and when ...
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... (a new validation **tag**, the "diff-**tag**" has to ... He validates (optically) each > response
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... (a new validation **tag**, the "diff-**tag**" has to be ... He validates (optically) each response
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www.cs.hut.fi/~ctl/moxy/documents/ts.html - 32k - Cached - Similar pages

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... Your test case will automatically generate an **HTML** table of test results. ... can also
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Evolutionary Divide and Conquer for the Set-Covering Problem - Hernández (1995) (Correct) (2 citations)
to try every single combination out. The **test plan** attempted to establish good parameter values by
www.dai.ed.ac.uk/groups/evalg/Projects/MSc/1994_95/luisgh/outline.ps.gz

Unknown - Th Annual Naccq (Correct)
7.1 Technical And Specification Testing The **Test Plan** For This Part Involves Checking The Integrity
WBMP images (e.g `Cross' image on the splash page)**Tag**Tag WAP Emulator (www.**tag**tag.com) is an online
image on the splash page)**Tag**Tag WAP Emulator (www.**tag**tag.com) is an online emulator used to simulate the
site.tekotago.ac.nz/staticdata/papers02/papers/goh225.pdf

Deliverable D6.1 Interim Report on the Results of the.. - Deliverable Type Pu (Correct)
in each activity and the definition of a **test plan**. The activities in which substantial work has
dedicated to a proprietary implementation by Cisco (**tag** switching)and has revealed that whilst the basic
By The Directly Connected Routers. Mpls Architecture **Tag** Sw Tching On 900 Kbps Atm Pvpc (2200 Ce L /s)
www.dante.net/quantum/qtp/QUA-99-070.pdf

Software Process Modeling using Functional Language Miranda - Matsuura, Honiden (Correct)
a design, Modifying a design, Modifying a **test plan**. TK: Exchanging opinions with others, Deciding
www.ipa.go.jp/NEWSOFT/public/EVA/jcse.ps.gz

. Principles . Objectives . Paradigms - Top Inspection Principles (Correct)
Vers. 1.2 Pages 2426 4. Kin (P) GDT System **Test Plan** Version 2 Sections 3.7 3.12 5. Checklists (S)
Members know the tools they should use Define **tags** for later use. Notes There is a Logical &
& Priority Sequence. 1-5 Document Type Document **Tag** Document Name Version /Date Chunk or Sample
www.pimsl.com/TomGilb/AdvancedInspections.pdf

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**1 Software process modelling: A precedence-based approach for proactive control in software   80%
process modelling**
Josep M. Ribó , Xavier Franch
**Proceedings of the 14th international conference on Software engineering and knowledge
engineering** July 2002
We present the proactive behavioural features of PROMENADE, a process modelling language
for formalizing the construction of software process models. PROMENADE aims at improving
expressiveness, standardization, flexibility and reuse in software process modelling. In this article
we focus on expressiveness, which is achieved by means of a declarative (instead of imperative)
proactive control-flow based on precedence relationships. Different families of such precedences
have been defined within th ...

**2 Multiprocessing design verification methodology for Motorola MPC74XX PowerPC   80%
microprocessor**
Jen-Tien Yen , Qichao Richard Yin
Proceedings of the 37th conference on Design automation June 2000
Multiprocessing (MP) design verification has been one of the bottlenecks for high performance
microprocessor design projects. The problem is getting worse as the design complexity increases
and more cache structures are integrated into one single chip. The challenges that MP
verification faces today include: huge chip/system simulation model sizes, long simulation cycles,
relative inefficiency of the simulation tools compared to uniprocessor, and so on. To solve these
challenging problems, ...

3 Testing large software with automated software evaluation systems   77%
C. V. Ramamoorthy , S. F. Ho
**ACM SIGPLAN Notices , Proceedings of the international conference on Reliable
software** April 1975
Volume 10 Issue 6

In the past few years, research has been actively carried out in an attempt to improve the quality and reliability of large scale software systems. Although progress has been made on the formal proof of program correctness, proving large scale software systems correct by formal proof is still many years away. Automated software tools have been found to be valuable in improving software reliability and attacking the high cost of software systems. This paper attempts to describe some main fea ...

4 Automated aids fob reliable software   77%

Donald J. Reifer

ACM SIGPLAN Notices , Proceedings of the international conference on Reliable software April 1975

Volume 10 Issue 6

Recent investigations on the use of automation to realize the twin objectives of cost reduction and reliability improvement for computer programs developed for the U. S. Air Force are reported. The concepts of reliability and automation as they pertain to software are explained. Then, over twenty automated tools and techniques (aids) identified by this investigation are described and categorized. Based on the information reviewed, an assessment of the state of the technology is made. Finall ...

5 An approach to document processing   77%

Robert Katz

Proceedings of the 2nd annual international conference on Systems documentation April 1983

6 Effective software engineering pedagogy   77%

Evelyn Stiller , Cathie LeBlanc

The Journal of Computing in Small Colleges May 2002

Volume 17 Issue 6

Accreditation standards have required computer science departments to teach software engineering for over a decade now. Industry statistics, however, suggest that good software engineering techniques often are not being carried out in large-scale software development projects, resulting in unusable software systems on which millions of dollars have been squandered. Society cannot afford to continue to squander precious resources on software fiascoes. We suggest that perhaps how we teach software ...

7 Pair programming and pair trading: effects on learning and motivation in a CS2 course   77%

Timothy H. DeClue

The Journal of Computing in Small Colleges May 2003

Volume 18 Issue 5

Pair programming is a seemingly powerful addition to the tool kit of educators wishing to enhance learning in software engineering students. This technique, found among professional software development practices associated with extreme programming (Beck), has probably existed informally for some time in various forms of collaborative software development. More recently pair programming has been formalized and used as an intentional pedagogical tool in the classroom. During the Spring semester o ...

8 Book review: Review of: Back to the User: Creating User-Focused Web Sites   77%

9 Abstraction-based software development   77%
Valdis Berzins , Michael Gray , David Naumann
Communications of the ACM May 1986
Volume 29 Issue 5
A five-year experience with abstraction-based software-development techniques in the university
environment indicates that the investment required to support the paradigm in practice is
returned in terms of greater ability to control complexity in large projects—provided there
exists a set of software tools sufficient to support the approach.

10 Teaching future verification engineers: the forgotten side of logic design   77%
Fusun Ozguner , Duane Marhefka , Joanne DeGroat , Bruce Wile , Jennifer Stofer , Lyle
Hanrahan
Proceedings of the 38th conference on Design automation June 2001
This paper describes a senior/graduate level course in hardware logic verification being offered
by The Ohio State University in cooperation with IBM. The need for the course is established
through the growing importance of logic verification to users of custom logic designs. We
discuss the short-term and long-term goals for the course, and describe the course content and
format. The course relies heavily on lab projects to illustrate the main concepts. Three projects
and a final project re ...

11 Information systems curriculum recommendations for the 80s: undergraduate and graduate   77%
programs
Jay F. Nunamaker , J. Daniel Couger , Gordon B. Davis
Communications of the ACM November 1982
Volume 25 Issue 11
The recommendations of the 1972 and 1973 ACM Curriculum Committee on Information
Systems programs have been influential in the development of degree programs at the bachelor's,
master's, and doctoral levels. The earlier curriculum has been revised and updated based on
advances in the field over the past nine years. The report discusses the continuing need for
education related to the definition, analysis, design, construction, and management of
information systems in organizations. The stru ...

12 Mining features for sequence classification   77%
Neal Lesh , Mohammed J. Zaki , Mitsunori Ogihara
**Proceedings of the fifth ACM SIGKDD international conference on Knowledge discovery
and data mining** August 1999

13 An object-oriented approach to automated generation of challenge examinations using Ada 95   77%
Arthur Irving Littlefield
ACM SIGAda Ada Letters January 1997
Volume XVII Issue 1
The primary objective of this paper is to analyze and evaluate the usefulness of object-oriented
development and the Ada 95 programming language as applied to a specific software
development project. A secondary objective is to show that structured development is still useful

while applying object-oriented development and that the two methods can be integrated. The project is to develop an automated tool for generation of challenge examinations to test the knowledge of students in a given subjec ...

14 Developing single-source documentation for multiple formats   77%
Cindy Roposh , Hanna Schoenrock
Proceedings of the 14th annual international conference on Systems documentation: Marshaling new technological forces: building a corporate, academic, and user-oriented triangle October 1996

15 A real world conversion to SGML   77%
Dee Stribling , Tim Hunter , Len Olszewski , Anne Corrigan , Randy Mullis , Lloyd Allen
Proceedings of the 14th annual international conference on Systems documentation: Marshaling new technological forces: building a corporate, academic, and user-oriented triangle October 1996

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Results:
Journal or Magazine = **JNL**   Conference = **CNF**   Standard = **STD**

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1 **Systems engineering and software development for the NIH fetal saturation (FOX) trial**
Weininger, S.;
Bioengineering Conference, 2003 IEEE 29th Annual, Proceedings of ,
22-23 March 2003
Page(s): 231 -232

[Abstract]   [PDF Full-Text (273 KB)] **IEEE CNF**

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2 **Test and evaluation experience with an untethered supervisory controlled undersea robot**
Walton, J.M.;
OCEANS '96. MTS/IEEE. 'Prospects for the 21st Century'. Conference Proceedings , Volume: 2 , 23-26 Sept. 1996
Page(s): 703 -710 vol.2

[Abstract]   [PDF Full-Text (616 KB)] **IEEE CNF**

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3 **The current status of the IEC 1000-4-X Basic EMC Standards**
Hopkins, M.;
Electromagnetic Compatibility Proceedings, 1997 International Symposium on , 21-23 May 1997
Page(s): 64 -67

[Abstract]   [PDF Full-Text (276 KB)] **IEEE CNF**

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4 **Rotorcraft flight test automation-the saga continues**

Carico, D.;
Aerospace Conference, 1998. Proceedings., IEEE , Volume: 3 , 21-28
March 1998
Page(s): 29 -38 vol.3

[Abstract]   [PDF Full-Text (628 KB)] **IEEE CNF**

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5 **Automated generation of SPICE characterization test masks and test databases**

Kasel, L.; McAndrew, C.C.; Drennan, P.; Davis, W.F.; Ida, R.;
Microelectronic Test Structures, 1999. ICMTS 1999. Proceedings of
the 1999 International Conference on , 15-18 March 1999
Page(s): 74 -79

[Abstract]   [PDF Full-Text (604 KB)] **IEEE CNF**

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6 **“…but can you prove it?” [technical documentation]**

Floyd, R.E.;
Professional Communication Conference, 2000. Proceedings of 2000
Joint IEEE International and 18th Annual Conference on Computer
Documentation (IPCC/SIGDOC 2000) , 24-27 Sept. 2000
Page(s): 309 -313

[Abstract]   [PDF Full-Text (280 KB)] **IEEE CNF**

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7 **Testing Java monitors through deterministic execution**

Harvey, C.; Strooper, P.;
Software Engineering Conference, 2001. Proceedings. 2001 Australian
, 27-28 Aug. 2001
Page(s): 61 -67

[Abstract]   [PDF Full-Text (504 KB)] **IEEE CNF**

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8 **IEEE standard for software test documentation**

IEEE Std 829-1998 , 16 Dec. 1998

[Abstract]   [PDF Full-Text (592 KB)] **IEEE STD**

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9 **A Management Information System for the National Ocean Service's Next Generation Water Level Measurement System (NGWLMS)**

Klopfenstein, R.;
OCEANS , Volume: 18 , Sep 1986
Page(s): 364 -369

10 Standard processes developed to insure the quality of software through system test

Gartner, S.J.; Droupa, D.C.;
Global Telecommunications Conference, 1988, and Exhibition.
'Communications for the Information Age.' Conference Record,
GLOBECOM '88., IEEE , 28 Nov.-1 Dec. 1988
Page(s): 451 -455 vol.1

11 Automated tool support for ANSI/IEEE STD: 829-1983 software test documentation

Sneed, H.M.;
Software Engineering Standards Symposium, 1993. Proceedings.,
1993 , 30 Aug.-3 Sept. 1993
Page(s): 308 -316

12 Automated validation test generation

Weber, R.; Thelen, K.; Srivastava, A.; Krueger, J.;
Digital Avionics Systems Conference, 1994. 13th DASC., AIAA/IEEE ,
30 Oct.-3 Nov. 1994
Page(s): 99 -104

13 Distributed techniques in software systems integration

Ramamoorthy, C.V.;
Distributed Computing Systems, 1995., Proceedings of the Fifth IEEE
Computer Society Workshop on Future Trends of , 28-30 Aug. 1995
Page(s): 252 -256

14 Aerospace nickel-cadmium cell verification-an update

Manzo, M.A.; Hall, S.W.;
Energy Conversion Engineering Conference, 1997. IECEC-97.
Proceedings of the 32nd Intersociety , Volume: 1 , 27 July-1 Aug.
1997
Page(s): 99 -107 vol.1

[Abstract]   [PDF Full-Text (624 KB)] **IEEE CNF**

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15 **A goal tree based high-level test planning system for DSP real number models**
Lin, M.; Armstrong, J.R.; Gray, F.G.;
Test Conference, 1998. Proceedings. International , 18-23 Oct. 1998
Page(s): 1000 -1009

[Abstract]   [PDF Full-Text (1824 KB)] **IEEE CNF**

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16 **Aerospace nickel-cadmium cell verification-final report**
Manzo, M.A.; Strawn, D.M.; Hall, S.W.;
Applications and Advances, 2001. The Sixteenth Annual Battery
Conference on , 9-12 Jan. 2001
Page(s): 59 -66

[Abstract]   [PDF Full-Text (624 KB)] **IEEE CNF**

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17 **How to conduct your own usability study**
Ricks, K.; Arnoldy, B.A.;
Professional Communication Conference, 2002. IPCC 2002.
Proceedings. IEEE International , 17-20 Sept. 2002
Page(s): 115 -126

[Abstract]   [PDF Full-Text (835 KB)] **IEEE CNF**

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18 **Effects of reuse on quality, productivity, and economics**
Lim, W.C.;
Software, IEEE , Volume: 11 Issue: 5 , Sept. 1994
Page(s): 23 -30

[Abstract]   [PDF Full-Text (684 KB)] **IEEE JNL**

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